

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: <b>Leah M. P. Pastel</b>	Date: <b>May 8, 2009</b>
Serial Number: <b>10/595,526</b>	Examiner: <b>Roberto Velez</b> Confirmation No.: <b>2272</b>
Filed: <b>04/26/2006</b>	Group Art Unit: <b>2829</b>
Title: <b>Hot-Switchable Voltage Bus for Performing a Quiescent Current (IDDQ) Test In An Integrated Circuit Chip</b>	IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

### AMENDMENT AFTER FINAL

Commissioner of Patents and Trademarks  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated March 18, 2009, the following Amendment is being submitted to place the above-identified application in prima facie condition for allowance.

Kindly consider the following Remarks:

Please enter /Roberto Velez/ 06/22/2009
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